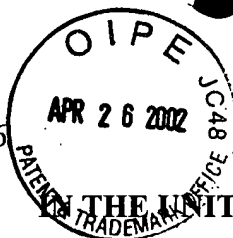


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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Stephen M. GATES

Allowed: April 8, 2002

Serial No.: 09/823,843

Group Art Unit: 2823

Filing Date: March 25, 1997

Examiner: Scott A. Brairton

For: LOW TEMPERATURE PROCESSES FOR MAKING ELECTRONIC DEVICE
STRUCTURES

Commissioner of Patents
Washington, D.C. 20231

SUBMISSION OF CORRECTED FORMAL DRAWINGS

Sir:

Submitted herewith are corrected formal drawings for Figures 1A-7B for the above-
identified patent application, which remedy the deficiencies noted on Form PTO-948, dated
April 3, 2002.

Approval and acknowledgment of receipt are respectfully requested.

Respectfully submitted,

Phillip E. Miller
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Date:

4/26/02

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